

SEP 14 2005

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APPLICANT(S): Brad R. Lewis, et al. ATTY. DOCKET NO. 30014200-1006  
SERIAL NO. 10/010,426 GROUP ART UNIT: 2193  
FILING DATE: November 8, 2001 EXAMINER: L. J. Shrader  
INVENTION: "METHODS AND SYSTEMS FOR DEVELOPING DATA FLOW PROGRAMS"

Original will NOT be mailed

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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PROGRAMS"

**AGENDA FOR TELEPHONIC INTERVIEW**

Commissioner of Patents  
P.O. Box 1450  
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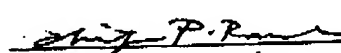
S I R:

In anticipation of the telephonic interview scheduled for September 14, 2005, Applicants submit this proposed agenda.

Applicants propose to discuss claim 29 in light of the cited references, particularly *Calder, et al. (U.S. Patent No. 5,963,972)*.

Claim 29 claims generating a graph comprising nodes corresponding to blocks of memory. Although *Calder* teaches a graph, *Calder's* graph does not include nodes that correspond to blocks of memory. Instead, *Calder's* nodes correspond to instructions, which are placed in cache memory locations after the graph has been created. (See, *Calder* 4:41-58).

Respectfully submitted,

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